

FIG. 1 is a block diagram of a system 10.

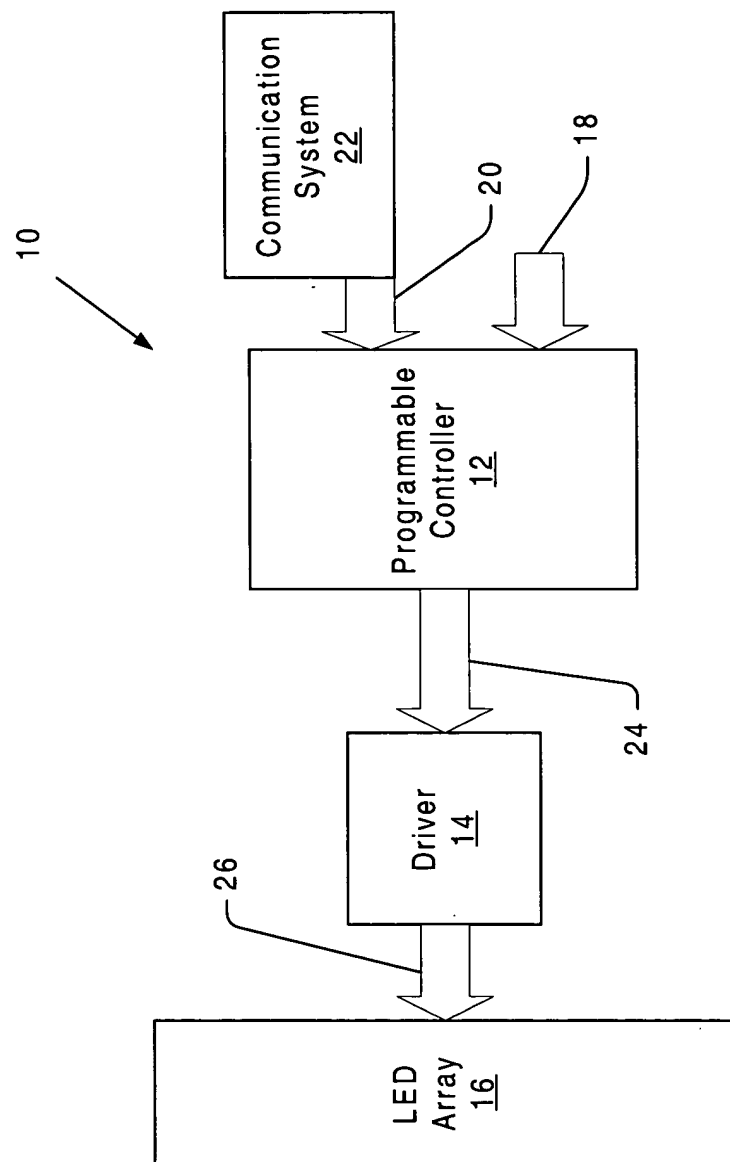


FIG. 1

FIG. 2 is a block diagram of a system 50. The system 50 includes a driver 14, an LED 46, and a plurality of ports 31, 33, 35. The driver 14 is connected to the LED 46 and the ports 31, 33, 35. The ports 31, 33, 35 are connected to a plurality of LEDs 28, 34, 40, 42. The LEDs 28, 34, 40, 42 are arranged in a grid. The system 50 is shown in FIG. 2.

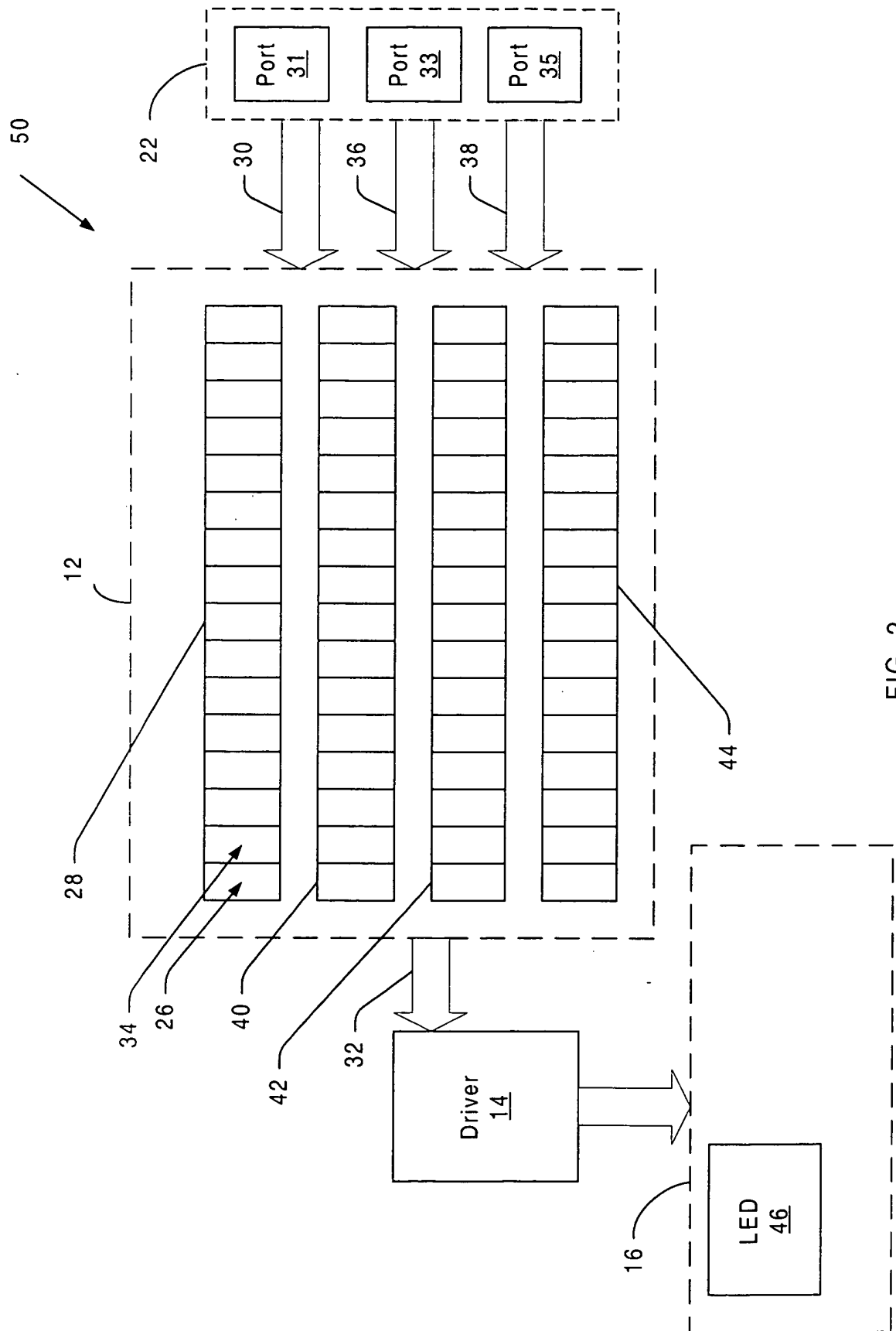


FIG. 2

FIG. 3 is a schematic diagram of a system 100 for controlling a plurality of devices 78. The system 100 includes a counter 62, a plurality of switches 70, a plurality of relays 72, a plurality of diodes 46, and a plurality of LEDs 78. The counter 62 is connected to the switches 70 via a bus 64. The switches 70 are connected to the relays 72 via a bus 66. The relays 72 are connected to the diodes 46 via a bus 68. The diodes 46 are connected to the LEDs 78 via a bus 74. The LEDs 78 are connected to a power source 76. The system 100 is controlled by a control signal 73.

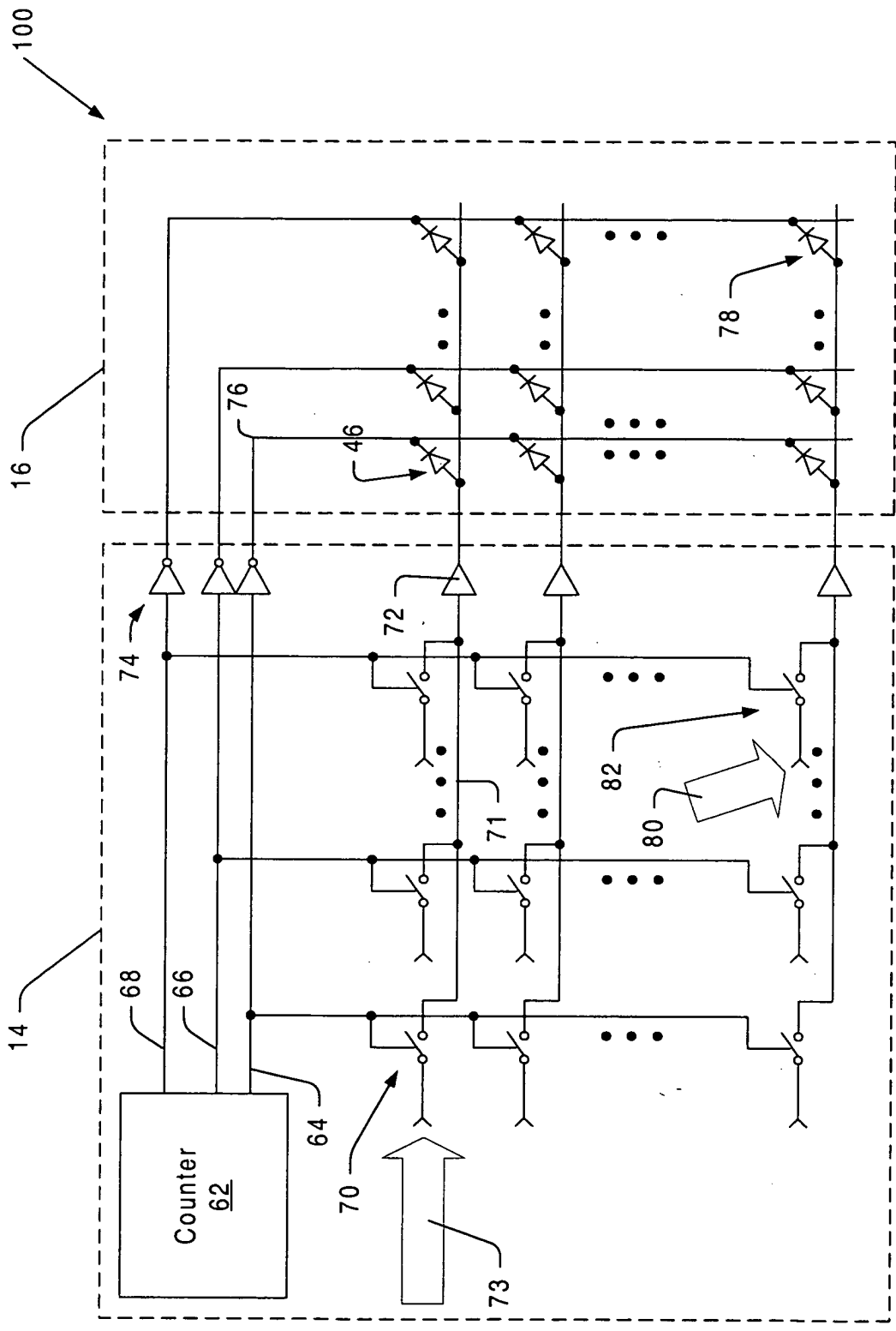
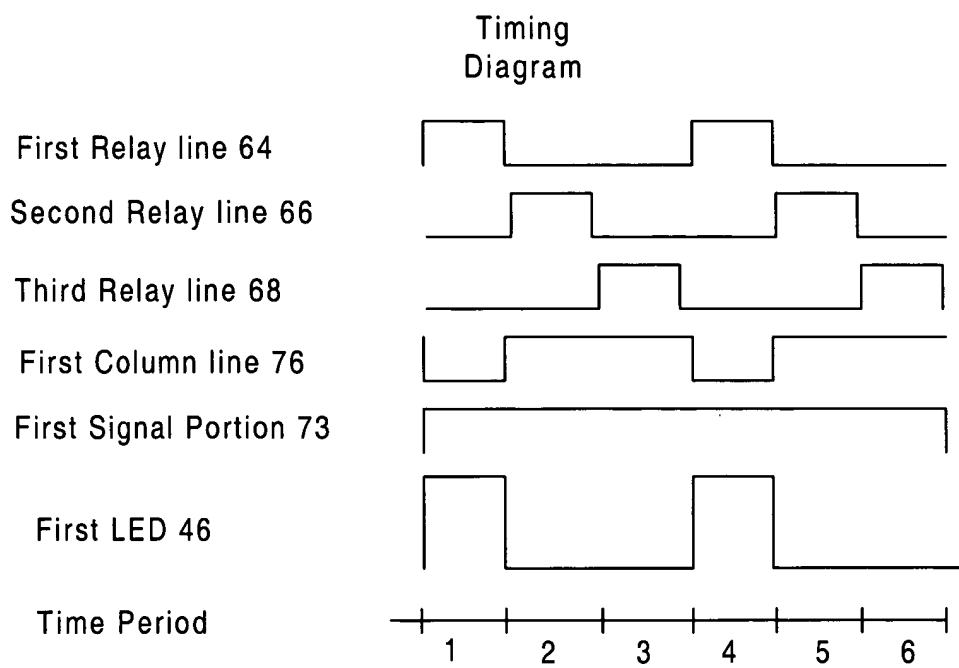


FIG. 3

FIG. 4 is a timing diagram showing the timing of the first relay line 64, the second relay line 66, the third relay line 68, the first column line 76, the first signal portion 73, the first LED 46, and the time period. The time period is divided into six equal intervals, numbered 1 through 6. The first relay line 64 is high during intervals 1 and 4, and low during intervals 2, 3, 5, and 6. The second relay line 66 is high during intervals 2 and 5, and low during intervals 1, 3, 4, and 6. The third relay line 68 is high during intervals 3 and 6, and low during intervals 1, 2, 4, and 5. The first column line 76 is high during intervals 1 and 4, and low during intervals 2, 3, 5, and 6. The first signal portion 73 is high during intervals 1 and 4, and low during intervals 2, 3, 5, and 6. The first LED 46 is high during intervals 1 and 4, and low during intervals 2, 3, 5, and 6.



100

FIG. 4